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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						Yes	No
Oru	EP 0 471 380	February 19, 1992	Europe			English Document	

OTHER DOCUMENTS

EXAMINER INITIAL	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.	
Oru	Akihiko Takase et al. "Datapath architecture and technology for large scale ATM switching systems" Global Telecommunications Conference, 1996. Globecom '96. Communications: The Key to Global Prosperity London, UK 18-22 Nov. 1996, New York, NY, USA, IEEE, US, 18 November 1996, pages 1395-1399.	
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	Syng-Jyan Wang "Load-Balancing in Multistage Interconnection Networks under Multiple-Pass Routing" Journal of Parallel and Distributed Computing, Academic Press, Duluth, MN, US, vol. 36, no. 2, 1 August 1996, pages 189-194.	
Oru	European Search Report, issued January 13, 2005.	

EXAMINER

DATE CONSIDERED

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